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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/628,921	07/29/2003	John J. Breen	16356.817 (DC-05156)	8117
27683	7590 06/05/2006		EXAMINER	
HAYNES AND BOONE, LLP			GRANT, ROBERT J	
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Please find below and/or attached an Office communication concerning this application or proceeding.

	Applicati n No.	Applicant(s)	
	10/628,921	BREEN ET AL.	
Office Action Summary	Examiner	Art Unit	
	Robert Grant	2838	
The MAILING DATE of this communication app Period for Reply	pears on the cover sheet with the c	orrespondence addres	SS
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING DA. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If NO period for reply is specified above, the maximum statutory period v. - Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tin will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	N. nely filed the mailing date of this commu D (35 U.S.C. § 133).	
Status			
1) Responsive to communication(s) filed on <u>09 Jac</u> 2a) This action is FINAL . 2b) This 3) Since this application is in condition for alloware closed in accordance with the practice under E	action is non-final. nce except for formal matters, pro		erits is
Disposition of Claims			
4) ☐ Claim(s) 1-23 is/are pending in the application. 4a) Of the above claim(s) is/are withd 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-6,9-17 and 19-23 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/o	rawn from consideration.		
Application Papers			
9) The specification is objected to by the Examine 10) The drawing(s) filed on 29 July 2003 is/are: a) Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Ex	☑ accepted or b) ☐ objected to be drawing(s) be held in abeyance. See tion is required if the drawing(s) is obj	e 37 CFR 1.85(a). jected to. See 37 CFR 1	
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priority application from the International Bureau * See the attached detailed Office action for a list	s have been received. s have been received in Applicati rity documents have been receive u (PCT Rule 17.2(a)).	on No ed in this National Sta	ge
Attachment(s) I)	4) 🔲 Interview Summary		
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) B) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date	Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:	ate latent Application (PTO-152	2)

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DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 2. Claims 1, 3, and 4 rejected under 35 U.S.C. 102(b) as being anticipated by Sainsbury et al. (US 6,104,162).

As to Claim 1, Sainsbury discloses a method for converting an alternating current (AC) input to a direct current (DC) output, the DC output providing power to a load, the method comprising: Receiving the alternative current (AC) input (Figure 3, element 11); Receiving a first feedback signal indicative of a target voltage required by the load (Column 5, lines 17-21); Receiving a second feedback signal indicative of the DC output (Column 5, lines 1-2); providing a controller module (Figure 5) included in an AC-DC adapter (Figure 3, element 22) and operable to receive the first feedback signal (Column 5, lines 17-21) and the second feedback (Column 5, lines 1-2); providing a converter in the AC-DC adapter (Figure 3, element 23); the controller module (Figure 5) adjusting a control signal, responsive to receiving the first and second feedback signals, to the converter to maintain the DC output within a predefined range of the target voltage (Column 4, lines 62-66).

As to Claim 3, Sainsbury discloses the method of claim 1, wherein the second DC output provides power to the load, wherein the load is a battery (Figure 6, Element 41).

As to Claim 4, Sainsbury discloses the method of claim 3, wherein the DC output is suitable to charge the battery (Column 5, lines 59-62).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 2 is rejected under 35 U.S.C. 103(a) as being unpatentable over Sainsbury (US 6,104,162) as applied to Claim 1 above, and further in view of Wilcox et al. (US 5,994,885).

As to Claim 2, Sainsbury discloses all the limitations of claim 1, as described above. However, Sainsbury does not disclose wherein a difference between the DC output and the target voltage is always positive while providing a charge to the load. Wilcox discloses wherein a difference between the DC output and the target voltage is always positive while providing a charge to the load (Column 4, line 30-33). It would have been obvious to a person having ordinary skill in the art at the time of the

invention to modify Sainsbury's charger with Wilcox's design to adjust the charging parameters in this fashion to make up for any losses.

Claims 5, 6, and 8-11 are rejected under 35 U.S.C 103(a) as being unpatentable over Sainsbury (US 6,1,04,162) as applied to Claim 1 above, and further in view of Shyr et al. (US 5,903,764)

As to Claim 5, Sainsbury discloses all the limitations of claim 1, as described above. However, Sainsbury does not expressly disclose wherein upon a loss of the first feedback signal the second DC output is maintained to a predefined voltage. Shyr discloses in column 1, lines 59-65, a smart battery which periodically responds to polling (i.e. feedback signals are not always present). It would have been obvious to a person having ordinary skill in the art at the time of the invention to modify Sainsbury method and utilize a smart battery as taught by Shyr, since periodically sending a feedback signal is more efficient.

As to Claim 6, see remarks for claim 5. A smart battery is capable of sending a signal indicating the voltage required and receiving that voltage back. If the signal is no longer present the battery will still be receiving that voltage.

As to Claim 8, Shyr discloses in column 1 lines 63-65 that the first feedback signal is received from the load.

As to Claim 9, Shyr discloses a smart battery, which would inherently have a controller for the gathering and transmitting the status and requirements of the battery.

As to Claim 10, Sainsbury discloses all the limitations of claim 1, as described above. However, Sainsbury does not expressly disclose wherein the first feedback signal is received as a single digital signal, a pulse width modulation (PWM) signal, an analog signal, a digital signal signal superimposed on another analog signal, or an SMBus signal. Shyr expressly disclose Column 6, tines 60-65 wherein the first feedback signal is received as a SMBus signal.

As to Claim 11, Sainsbury discloses all the limitations of claim 1, as described above. However, Sainsbury does not expressly disclose wherein the DC output is maintained at a predefined voltage upon completion of providing a charge to the load. Shyr expressly discloses figure 10a, element 374, wherein the DC output is maintained at a predefined voltage upon completion of providing a charge to the load (i.e. trickle charge).

Claim 12,14, and 15 are rejected under 35 U.S.C 103(a) as being unpatentable over Sainsbury (US 6,104,162) in view of Hatular (US 6,184,660).

As to Claim 12, Sainsbury discloses an integrated alternating current (AC) to direct current (DC) adapter comprising: A rectifier module operable to receive an AC input and generate a first DC output (Figure 4, element 29), an AC-DC adapter (Figure 3, element 22); and a controller module (Figure 5) included in the AC-DC adapter (Figure 3, element 22). Sainsbury does not expressly disclose a buck converter module operable to receive the first DC output and generate a second DC output responsive to a control signal; and a controller module operable to receive the first feedback signal

input indicative of a target voltage required by a load and a second feedback signal input indicative of the second DC output, the controller module adjusting the control signal responsive to the first and second feedback signal inputs, to the buck converter module to maintain the second DC output to be within a predefined range of the target. Hatular expressly discloses the use of a buck converter (Figure 1A., Element 60) to supply power for charging a battery. It would have been obvious to a person having ordinary skill in the art at the time of the invention to modify Sainsbury's design by replacing his DC-DC converter with Hatular's buck converter. This replacement in Sainsbury's design would then yield: a buck converter module operable to receive the first DC output and generate a second DC output responsive to a control signal (Sainsbury: Column 5 lines 17-21); and a controller module included in the AC-DC adapter and operable to receive the first feedback signal input indicative of a target voltage required by a load (Hatular: Figure 1A, Element 50) and a second feedback signal input indicative of the second DC output (Sainsbury: Column 5 lines 1-2), the controller adjusting the control signal responsive to the first and second feedback signal inputs, the adjusting of the control signal causing the buck converter module to maintain the second DC output to be within a predefined range of the target (Sainsbury: Column 4, lines 62-66).

As to Claim 14, Sainsbury discloses wherein the second DC output provides power to the load, wherein the load is a battery (Figure 6, Element 41).

As to Claim 15, Sainsbury discloses the method of claim 14, wherein the second DC output is suitable to charge the battery (Column 5, lines 59-62).

Claim 13 is rejected under 35 U.S.C. 103(a) as being unpatentable over Sainsbury in view of Hatular as applied to claim 12 above, and further in view of Wilcox et al. (US 5,994,885).

As to Claim 13, Sainsbury and Hatular disclose all the limitations of claim 12, as described above. However, Sainsbury and Hatular do not disclose wherein a difference between the DC output and the target voltage is always positive while providing a charge to the load. Wilcox discloses wherein a difference between the DC output and the target voltage is always positive while providing a charge to the load (Column 4, line 30-33). It would have been obvious to a person having ordinary skill in the art at the time of the invention to modify Sainsbury and Hatular charger with Wilcox's design to adjust the charging parameters in this fashion to make up for any losses.

Claim 16, 17, 19, and 20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sainsbury in view of Hatular as applied to claim 12 above, and further in view of Shyr.

As to Claim 16, Sainsbury and Hatular disclose all the limitations of claim 12, as described above. Neither Sainsbury nor Hatular expressly disclose wherein upon a loss of the first feedback signal the controller generates the control signal to maintain the second DC output to a predefined voltage. Shyr discloses in column 1 lines 59-65 a smart battery which periodically responds to polling (i.e. feedback signals are not

always present). It would have been obvious to a person having ordinary skill in the art at the time of the invention to modify Sainsbury method and utilize a smart battery as taught by Shyr, since periodically sending a feedback signal is more efficient.

As to Claim 17, see remarks for claim 16. A smart battery is capable of sending a signal indicating the voltage required and receiving that voltage back. If the signal is no longer present the battery will still be receiving that voltage.

As to Claim 19, Shyr discloses wherein the first feedback signal is received from the load (Column 1, lines 63-65)

As to Claim 20, Shyr discloses a smart battery, which would inherently have a controller for the gathering and transmitting the status and requirements of the battery.

Claim 21 is rejected under 35 U.S.C. 103(a) as being unpatentable over Sainsbury in view of Hatular as applied to claim 12 above, and further in view of Shyr.

As to Claim 21, Sainsbury and Hatular discloses all the limitations of claim 12, as described above. Neither Sainsbury nor Hatular expressly disclose wherein the first feedback signal is received as a single digital signal, a pulse width modulation (PWM) signal, an analog signal, a digital signal superimposed on another analog signal, or an SMBus signal. Shyr expressly disclose Column 6, lines 60-65 wherein the first feedback signal is received as a SMBus signal.

Claim 22 is rejected under 35 U.S.C. 103(a) as being unpatentable over Hatular in view of Sainsbury.

Hatular expressly discloses in figure 1 element 26, an information handling system comprising:

A processor; (inherent to an information handling system)

A system bus; (inherent to an information handling system)

A memory coupled to the processor through the system bus (inherent to an information handling system) (Column 5, Lines 34-40); and

A power supply system operable to provide power to the processor, the bus and memory, the power supply system being connectable to an alternating current (AC) power source, wherein the power supply system includes (Column 5, lines 24-29):

A buck converter module (Element 60) operable to receive the first DC output and generate a second DC output.

Hatular does not expressly disclose a rectifier module for receiving the AC input and generate DC output, a control module for receiving feedback signals and controlling the output of the buck converter.

Sainsbury expressly discloses a rectifier module (Figure 4, Element 29) operable to receive the AC input and generate a first direct current (DC) output; an AC-DC adapter (figure 3, element 22); a control module (Figure 5) included in the AC-DC adapter (figure 3, element 22) and operable to receive the first feedback signal (Vsense) input indicative of a target voltage required and a second feedback signal (Vsel) input indicative of the second DC output (Column 5 lines 1-2), the controller module adjusting

the control signal responsive to the first and second feedback signal inputs (Figure 4, Elements 32, 33, 34) to causing the second DC output to be within a predefined range of the target voltage (Column 4, lines 62-66). It would have been obvious at the time of this invention to combine the teachings of Sainsbury's multiple power source with the Hatular's smart battery computer system, in order to be able to regulate the voltage going into the system.

Claim 23 is rejected under 35 U.S.C. 103(a) as being unpatentable over Hatular in view of Sainsbury.

Hatular expressly discloses in figure 1 element 26, an information handling system comprising:

A processor; (inherent to an information handling system)

A system bus; (inherent to an information handling system)

A memory coupled to the processor through the system bus (inherent to an information handling system) (Column 5, Lines 34-40); and

A power supply system operable to provide power to the processor, the bus and memory, the power supply system being connectable to an alternating current (AC) power source, wherein the power supply system includes (Column 5, lines 24-29):

A buck converter module (Element 60) operable to receive the first DC output and generate a second DC output.

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Hatular does not expressly disclose a rectifier module for receiving the AC input and generate DC output, a control module for receiving feedback signals and controlling the output of the buck converter.

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Sainsbury expressly discloses a rectifier module (Figure 4, Element 29) operable to receive the AC input and generate a first direct current (DC) output; an AC-DC adapter (Figure 3, element 22) a control module (Figure 5) included in the AC-DC adapter (Figure 3, element 22) and operable to receive the first feedback signal (Vsense) input indicative of a target voltage required and a second feedback signal (Vsel) input indicative of the second DC output (Column 5 lines 1-2), the controller module adjusting the control signal responsive to the first and second feedback signal inputs (Figure 4, Elements 32, 33, 34) to the second DC output to be within a predefined range of the target voltage (Column 4, lines 62-66). It would have been obvious at the time of this invention to combine the teachings of Sainsbury's multiple power source with the Hatular's smart battery computer system, in order to be able to regulate the voltage going into the system.

Response to Arguments

- 1. Applicant's arguments filed 1-9-06 have been fully considered but they are not persuasive.
- 2. In response to applicant's argument that the examiner's conclusion of obviousness is based upon improper hindsight reasoning, it must be recognized that any judgment on obviousness is in a sense necessarily a reconstruction based upon

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hindsight reasoning. But so long as it takes into account only knowledge which was within the level of ordinary skill at the time the claimed invention was made, and does not include knowledge gleaned only from the applicant's disclosure, such a reconstruction is proper. See *In re McLaughlin*, 443 F.2d 1392, 170 USPQ 209 (CCPA 1971).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Robert Grant whose telephone number is 571-272-2727. The examiner can normally be reached on M-F 8:30-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Sherry can be reached on 571-272-2084. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

KARL EASTHOM SUPERVISORY PATENT EXAMINER